



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant:** Koichi TAMURA

**Docket:** 13392

#12

**Serial No.** 09/510,861

**Examiner:** Chieh M. Fan

**Filed:** February 23, 2000

**Group Art Unit:** 2634

**For:** DEMODULATION AND  
MODULATION CIRCUIT AND  
DEMODULATION AND MODULATION  
OF THE SAME

**Dated:** September 25, 2003

**RECEIVED**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2600

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

A review of the above application in preparation for filing a divisional application has revealed several typographical errors in the Information Disclosure Statements dated November 1, 2000 and August 2, 2001.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 25, 2003.

Dated: September 25, 2003

Paul J. Esatto, Jr.

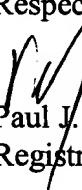
In the Information Disclosure Statement dated November 1, 2003, European Patent Application No. EP 0 716 520 A1 had an incorrect publication date. The correct date is **June 12, 1996.**

In the Information Disclosure Statement dated August 2, 2001, reference No. 7 should read Japanese Unexamined Patent Application No. 10-341263 dated December 22, 1998. Also, Japanese Unexamined Patent Application No. 8-111677 dated April 30, 1996 was inadvertently not listed on the PTO 1449 nor included in the Information Disclosure Statement. Please see the original Japanese Office Action dated May 29, 2001, page 7, as well as the English language translation on page 13 which references 8-111677.

A corrected PTO -1449 is enclosed.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

  
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